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1449/PTO	U.S. Department of Commerce Patent and Trademark Office	Application No. : Filing Date : First Named Inventor: K. NAGATANI Group Art Unit : Examiner Name : Attorney Docket No. : FUSA 19.236
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		
Sheet 1 of 1		

10/01/97
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U.S. PATENT DOCUMENTS

Examiner Initials	Cite No. ¹	U.S. Patent Document	Kind Code if known ²	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns Lines Where Relevant Passages or Relevant Figures Appear
<i>AB</i>		4,700,151		Nagata	10/13/87	
<i>AB</i>		5,903,611		Schnabl et al.	05/11/99	
<i>AB</i>		5,164,678		Puri et al.	11/17/92	

FOREIGN DOCUMENTS

Examiner Initials	Cite No. ¹	Foreign Patent Document Office ³ Number ⁴ Kind Code ⁵ (if known)	Country	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns Lines Where Relevant Passages or Relevant Figures Appear
<i>AB</i>		091164741	JP	Sony Corp.	05/02/97	
<i> </i>		10136048	JP	Hitachi Denshi Ltd.	05/22/98	
<i> </i>		62139425	JP	Nec Corp	06/23/87	
<i> </i>		63121326	JP	Nec Corp.	05/25/88	
<i> </i>		58084558	JP	Nec Corp.	05/20/83	
<i>AB</i>		10145146	JP	Matsushita Electric Ind. Co Ltd.	05/29/98	

Other Prior Art-Non Patent Literature Documents

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), data, page(s), volume-issue number(s), publisher, country, where published, source.	Applicant check her if English language translation attached
<i>B</i>		NAGATANI, ET AL. Simulated Tolerance of Loop Parameters of Cartesian Feedback Amplifier for $\pi/4$ Shifted QPSK Transmitter June 26, 1992, pgs 25-30	
<i>A</i>		IEEE Transactions on Signal Processing June 1998 Volume 6 pgs. 1726-1730	

Examiner Signature	<i>A. Behulu</i>	Date Considered	<i>8-18-04</i>
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